As a below named inventor, I declare: that I verily believe myself to be the original, first and sole (if only one individual inventor is listed below) or an original, first and joint inventor (if more than one individual inventor is listed below) of the invention in

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

the specification of which is attached hereto unless the following box is

□ was filed on _ as United States Application or PCT International Application No. was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment

I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365 (b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

Country Category Application No. Filing Date Priority Claim Japan Patent 2000-297703 September 28, 2000 Yes

And I hereby appoint Stuart Lubitz (Reg. No. 20,680), Louis A. Mok (Reg. No. 22,585), John P. Scherlacher (Reg. No. 23,009), William J. Byorick (Reg. No. 29,664), Stuart T. Langley (Reg. No. 33,940), Michael Peterson (Reg. No. 36,238), William H. Wright (Reg. No. 35,465), Steven C. K. Barton (Reg. No. 36,238), William H. Wright (Reg. No. 36,312), Steven Yang (Reg. No. 38,690), Sarah S. O'Rourke (Reg. No. 38,229), Wei-Ning G. Dyor (Reg. No. 42,278) and Ying Chen under 37 C. F. R. 10.9(b) each of Angeles, California 90071, or any one of them. my attorneys with full to transact all business in the Patent & Tradmark Office connected Hartson L. L. P., Biltmore Tower, 500 South Grand Avenue, Suite 1900, Los Angeles, California 90071. Please direct telephone calls to William H. Angeles, California 90071. Please direct telephone calls to William H.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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DECLARATION FOR PATENT APPLICATION

I declare further that my mailing address is at c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura 1-chome, Minato-ku, Tokyo 105-8001, Japan; and that my citizenship and residence are as stated below next to my name:

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Commissioner for Patents

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Art Unit:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Katsushi NAGABA and Shigeo OHSHIMA

Serial No: Not Assigned

Filed: September 25, 2001

For: Semiconductor Integrated Circuit Device

ASSOCIATE POWER OF ATTORNEY (37 C.F.R. § 1.34)

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Dear Sir:

Please recognize the following as my associate attorneys in the above-entitled application:

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Respectfully submitted,

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